

# Arm Cortex-R Processor Comparison Table

The Cortex-R series of processors deliver fast and deterministic processing and high performance, while meeting challenging real-time constraints in a range of situations. They combine these features in a performance, power and area optimized package, making them the trusted choice in reliable systems demanding high error-resistance.

Feature	<a href="#">Cortex-R4</a>	<a href="#">Cortex-R5</a>	<a href="#">Cortex-R7<sup>†</sup></a>	<a href="#">Cortex-R8</a>	<a href="#">Cortex-R52</a>	<a href="#">Cortex-R52+</a>	<a href="#">Cortex-R82</a>
Instruction Set Architecture	Armv7-R	Armv7-R	Armv7-R	Armv7-R	Armv8-R	Armv8-R	Armv8-R
Pipeline Depth	8 stage in-order, dual issue	8 stage in-order, dual issue	11 stage out-of-order, superscalar	11 stage out-of-order, superscalar	8 stage in-order, superscalar	8 stage in-order, superscalar	8 stage in-order, triple issue
Address Bits	32	32	32	32	32	32	40
Addressable Memory	4GB	4GB	4GB	4GB	4GB	4GB	1TB
ECC on Memories	Yes	Yes	Yes	Yes	Yes	Yes	Yes
MPU or MMU	MPU	MPU	MPU	MPU	MPU	MPU	Both
Maximum MPU Regions	12	16	16	24	24+24	24+24	32+32
Symmetric Multi-Processing (SMP) Support	1 core, No coherency	2 core, IO coherency	Up to MP2	Up to MP4	Up to UP4, No coherence	Up to UP4, No coherence	Up to MP8
Floating Point Unit (FPU)	Optional	Optional	Optional	Optional	Optional	Optional	Optional
SIMD (Neon)	No	No	No	No	Optional	Optional	Optional
DMIPS/MHz*	1.67	1.67	2.5	2.5	2.09	2.09	3.71
CoreMark®/MHz*	3.47	3.47	4.35	4.62	4.3	4.3	6.28
Maximum # External Interrupts	Up to 480	Up to 480	Up to 480	Up to 480	Up to 960	Up to 960	56K+
Bus Protocol	AXI3	AXI3	AXI3	AXI3	AXI4	AXI4	AXI5 and CHI-E

Feature	<a href="#">Cortex-R4</a>	<a href="#">Cortex-R5</a>	<a href="#">Cortex-R7<sup>†</sup></a>	<a href="#">Cortex-R8</a>	<a href="#">Cortex-R52</a>	<a href="#">Cortex-R52+</a>	<a href="#">Cortex-R82</a>
Instruction TCM	0–8MB	0–8MB	0–128KB	0–1MB	0–1MB	0–1MB	0,16KB–1MB
Data TCM	0–8MB	0–8MB	0–128KB	0–1MB	0–1MB	0–1MB	0,16KB–1MB
Instruction Cache	0,4KB–64KB	0,4KB–64KB	0,4KB–64KB	0KB–64KB	0KB–32KB	0KB–32KB	16KB–128KB
Data Cache	4KB–64KB	4KB–64KB	4KB–64KB	0KB–64KB	0KB–32KB	0KB–32KB	16KB–64KB
L2 Cache	N/A	N/A	N/A	N/A	N/A	N/A	0,128KB–4MB
Dual Core Lock-Step (DCLS)	No	Yes	No	No	Yes	Yes	No
Safety Package	No	Yes	No	No	Yes	Yes	No
Software Test Library	No	Yes	No	No	Yes	Yes	No

<sup>†</sup> Arm products undergo continual development and improvement. The Cortex-R7 processor is no longer available to license and is included here for comparison purposes only.

Cortex-R series processors support compatibility, enabling software reuse and migration as functionality and/or additional processing power is required.

\*See individual Cortex-R product pages for further information.

For more information, contact your Arm account manager today or explore the processors in more detail here: [developer.arm.com/ip-products/processors/cortex-r](https://developer.arm.com/ip-products/processors/cortex-r)



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